

Day : Thursday
Date: 9/29/2005

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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = FUJII

First Name = TERUKO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09205373	Not Issued	161	12/04/1998	REMOTE AUTHENTICATION SYSTEM	FUJII, TERUKO
09335056	6751733	150	06/16/1999	REMOTE AUTHENTICATION SYSTEM	FUJII, TERUKO
10031436	Not Issued	61	01/22/2002	Communication system and method using a master node designated based on transmission qualities between nodes	FUJII, TERUKO
10049287	Not Issued	30	02/11/2002	Communication system, communication device, and communication method	FUJII, TERUKO
10168728	Not Issued	30	06/24/2002	Communication system and communication method	FUJII, TERUKO

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = BABA

First Name = YOSHIMASA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07284438	5455568	250	12/14/1988	SELECTIVE COMMUNICATION SYSTEM HAVING SEPARATE CONNECTION CIRCUITS BETWEEN COMMUNICATION DEVICES	BABA, YOSHIMASA
08853919	6016350	150	05/09/1997	ENCRYPTION APPARATUS FOR ENABLING ENCRYPTION AND NON-ENCRYPTION TERMINALS TO BE CONNECTED ON THE SAME NETWORK	BABA, YOSHIMASA
09205373	Not Issued	161	12/04/1998	REMOTE AUTHENTICATION SYSTEM	BABA, YOSHIMASA
09335056	6751733	150	06/16/1999	REMOTE AUTHENTICATION SYSTEM	BABA, YOSHIMASA
10031436	Not Issued	61	01/22/2002	Communication system and method using a master node designated based on transmission qualities between nodes	BABA, YOSHIMASA
10049287	Not Issued	30	02/11/2002	Communication system, communication device, and communication method	BABA, YOSHIMASA
10168728	Not Issued	30	06/24/2002	Communication system and communication method	BABA, YOSHIMASA

Inventor Search Completed: No Records to Display.

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = NAGASHIMA

First Name = YASUYUKI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08695190	5740204	150	08/07/1996	DIGITAL RADIOCOMMUNICATION RECEIVER	NAGASHIMA, YASUYUKI
08738050	5715284	150	10/25/1996	DIGITAL RADIO COMMUNICATIONS RECEIVER	NAGASHIMA, YASUYUKI
08738796	5898743	150	10/28/1996	DIGITAL RADIO COMMUNICATIONS RECEIVER	NAGASHIMA, YASUYUKI
09455902	Not Issued	161	12/07/1999	APPARATUS AND METHOD FOR ACQUIRING FRAME SYNCHRONIZATION	NAGASHIMA, YASUYUKI
10031436	Not Issued	61	01/22/2002	Communication system and method using a master node designated based on transmission qualities between nodes	NAGASHIMA, YASUYUKI
10049287	Not Issued	30	02/11/2002	Communication system, communication device, and communication method	NAGASHIMA, YASUYUKI
10168728	Not Issued	30	06/24/2002	Communication system and communication method	NAGASHIMA, YASUYUKI
10203488	6827838	150	08/09/2002	METHOD OF SEPARATING AND COLLECTING 18F IN 180 WATER	NAGASHIMA, YASUYUKI

Inventor Search Completed: No Records to Display.

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Day : Thursday
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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = KATO

First Name = MASATAKA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06089263	4301755	150	10/26/1979	MULTIPLE PATTERN SEWING MACHINE	KATO, MASATAKA
06094880	4274348	150	11/16/1979	MULTIPLE PATTERN SEWING MACHINE	KATO, MASATAKA
06256400	4377120	150	04/22/1981	PATTERN SELECTION SYSTEM FOR A SEWING MACHINE	KATO, MASATAKA
06701160	Not Issued	161	02/13/1985	LATERAL BIPOLAR TRANSISTOR AND METHOD OF PRODUCING THE SAME	KATO, MASATAKA
07040125	4769687	150	04/20/1987	LATERAL BIPOLAR TRANSISTOR AND METHOD OF PRODUCING THE SAME	KATO, MASATAKA
08085156	Not Issued	166	07/02/1993	NON-VOLATILE SEMICONDUCTOR MEMORY	KATO, MASATAKA
08164780	5592415	150	12/10/1993	NON-VOLATILE SEMICONDUCTOR MEMORY	KATO, MASATAKA
08172255	5460059	150	12/23/1993	DRIVE APPARATUS	KATO, MASATAKA
08203303	5422856	150	03/01/1994	NON-VOLATILE MEMORY PROGRAMMING AT ARBITRARY TIMING BASED ON CURRENT REQUIREMENTS	KATO, MASATAKA
08249383	5446690	150	05/25/1994	SEMICONDUCTOR NONVOLATILE MEMORY DEVICE	KATO, MASATAKA
08252604	5467309	150	06/01/1994	SEMICONDUCTOR NONVOLATILE MEMORY DEVICE HAVING REDUCED SWITCHING OVERHEAD	KATO, MASATAKA

				TIME IN THE PROGRAM MODE	
<u>08354639</u>	<u>5532971</u>	150	12/13/1994	A NONVOLATILE SEMICONDUCTOR MEMORY HAVING ENHANCED SPEED FOR ERASING AND PROGRAMMING	KATO, MASATAKA
<u>08413263</u>	<u>5672529</u>	150	03/30/1995	METHOD OF MANUFACTURING NONVOLATILE SEMICONDUCTOR MEMORY DEVICE	KATO, MASATAKA
<u>08444511</u>	Not Issued	161	05/19/1995	SEMICONDUCTOR NONVOLATILE MEMORY DEVICE	KATO, MASATAKA
<u>08562021</u>	<u>5814543</u>	150	11/22/1995	A METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING NON VOLATILE MEMORY CELLS	KATO, MASATAKA
<u>08628817</u>	<u>5793678</u>	150	04/05/1996	PARALLEL TYPE NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND USING METHOD OF THE SAME	KATO, MASATAKA
<u>08677842</u>	<u>5748532</u>	150	07/10/1996	SEMICONDUCTOR NONVOLATILE MEMORY DEVICE AND COMPUTER SYSTEM USING THE SAME	KATO, MASATAKA
<u>08720016</u>	Not Issued	161	09/27/1996	NONVOLATILE SEMICONDUCTOR MEMORY DEVICE EQUIPPED WITH SENSE/LATCH CIRCUITS FOR VERIFYING THRESHOLD VOLTAGES OF MULTI VALUE MEMORY CELLS DURING PROGRAMMING AND ERASING OPERATIONS	KATO, MASATAKA
<u>08739156</u>	<u>5828600</u>	150	10/30/1996	NON-VOLATILE SEMICONDUCTOR MEMORY	KATO, MASATAKA
<u>08851536</u>	<u>5932909</u>	150	05/05/1997	NONVOLATILE SEMICONDUCTOR MEMORY DEVICE	KATO, MASATAKA
<u>08991603</u>	Not	161	12/16/1997	SEMICONDUCTOR	KATO,

	Issued			INTEGRATED CIRCUIT	MASATAKA
<u>09029748</u>	<u>5978270</u>	150	03/02/1998	SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE AND COMPUTER SYSTEM USING THE SAME	KATO, MASATAKA
<u>09060364</u>	<u>5872734</u>	150	04/15/1998	SEMICONDUCTOR NONVOLATILE MEMORY DEVICE AND COMPUTER SYSTEM USING THE SAME	KATO, MASATAKA
<u>09124794</u>	<u>5910913</u>	150	07/30/1998	NON-VOLATILE SEMICONDUCTOR MEMORY	KATO, MASATAKA
<u>09217811</u>	<u>6211546</u>	150	12/22/1998	METHOD OF MANUFACTURING NONVOLATILE SEMICONDUCTOR MEMORY DEVICE	KATO, MASATAKA
<u>09288313</u>	<u>6101123</u>	150	04/08/1999	NONVOLATILE SEMICONDUCTOR MEMORY WITH PROGRAMMING AND ERASING VERIFICATION	KATO, MASATAKA
<u>09362719</u>	<u>6201735</u>	150	07/29/1999	ELECTRICALLY ERASABLE AND PROGRAMMABLE NONVOLATILE SEMICONDUCTOR MEMORY	KATO, MASATAKA
<u>09402078</u>	<u>6461916</u>	150	02/07/2000	NON-VOLATILE SEMICONDUCTOR MEMORY AND METHOD FOR MAKING THE SAME, AND SEMICONDUCTOR DEVICE AND METHOD FOR MAKING THE SAME	KATO, MASATAKA
<u>09432460</u>	<u>6657248</u>	150	11/02/1999	SEMICONDUCTOR DEVICE HAVING GROOVE ISOLATION STRUCTURE AND GATE OXIDE FILMS WITH DIFFERENT THICKNESSES	KATO, MASATAKA
<u>09453079</u>	<u>RE37311</u>	150	12/02/1999	PARALLEL TYPE NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND METHOD OF USING THE SAME	KATO, MASATAKA
<u>09558102</u>	Not Issued	161	04/25/2000	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE	KATO, MASATAKA

<u>09573894</u>	Not Issued	161	05/19/2000	Multi-carrier communication system and multi-carrier communication method	KATO, MASATAKA
<u>09630426</u>	<u>6272042</u>	150	08/01/2000	Nonvolatile semiconductor memory	KATO, MASATAKA
<u>09638862</u>	Not Issued	161	08/15/2000	Communication method and communication system	KATO, MASATAKA
<u>09660923</u>	<u>6531735</u>	150	09/13/2000	SEMICONDUCTOR INTERGRATE CIRCUIT	KATO, MASATAKA
<u>09680936</u>	<u>6442070</u>	150	10/10/2000	SEMIDCONDUCTOR NONVOLATILE MEMORY APPARATUS AND COMPUTER SYSTEM USING THE SAME	KATO, MASATAKA
<u>09690737</u>	Not Issued	161	10/18/2000	Communications apparatus and a communications method	KATO, MASATAKA
<u>09791832</u>	<u>6420754</u>	150	02/26/2001	A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	KATO, MASATAKA
<u>09811598</u>	<u>6423584</u>	150	03/20/2001	METHOD FOR FORMING CAPACITORS AND FIELD EFFECT TRANSISTORS IN A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	KATO, MASATAKA
<u>09819803</u>	<u>6406958</u>	150	03/29/2001	METHOD OF MANUFACTURING NONVOLATILE SEMICONDUCTOR MEMORY DEVICE	KATO, MASATAKA
<u>09849992</u>	<u>6680979</u>	150	05/08/2001	METHOD AND DEVICE FOR COMMUNICATION	KATO, MASATAKA
<u>09880934</u>	<u>6370059</u>	150	06/15/2001	Nonvolatile semiconductor memory	KATO, MASATAKA
<u>09881020</u>	<u>6335880</u>	150	06/15/2001	Nonvolatile semiconductor memory	KATO, MASATAKA
<u>09967928</u>	Not Issued	161	10/02/2001	Semiconductor integrated circuit device and a method of manufacturing the same	KATO, MASATAKA
<u>09983803</u>	<u>6626571</u>	150	10/25/2001	DUSTPROOF CONSRUCTION FOR SLIDER UNIT	KATO, MASATAKA
<u>09984816</u>	<u>6538926</u>	150	10/31/2001	NONVOLATILE SEMICONDUCTOR MEMORY SYSTEM WITH CAPABILITY OF STARTING A NEW	KATO, MASATAKA

				PROGRAM OPERATION WHILE AN EXISTING PROGRAM OPERATION IS BEING PERFORMED	
<u>09984918</u>	<u>6510086</u>	150	10/31/2001	NONVOLATILE SEMICONDUCTOR MEMORY	KATO, MASATAKA
<u>10011731</u>	<u>6444554</u>	150	12/11/2001	METHOD OF MAKING A NON-VOLATILE MEMORY AND SEMICONDUCTOR DEVICE	KATO, MASATAKA
<u>10031436</u>	Not Issued	61	01/22/2002	Communication system and method using a master node designated based on transmission qualities between nodes	KATO, MASATAKA
<u>10049287</u>	Not Issued	30	02/11/2002	Communication system, communication device, and communication method	KATO, MASATAKA

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L3	0	L2 and 375/220.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 15:37
L4	1	L2 and 375/222.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 15:37
L5	1	L2 and 375/257.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 15:38
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